

## Relevant Skills

### Areas of Knowledge

Analog & Mixed-signal IC Design, RF IC Design, Discrete PCB Design, Embedded Systems, Firmware & Software.

### Technical Skills

Synopsys (UDE), Cadence Virtuoso (Spectre), SPICE, VerilogA, Linux, TCL, Verilog, Python, SHELL scripting, MATLAB, Altium Designer, Eagle, KiCad, C,  $\text{\LaTeX}$ .

### Personal Skills

English and Spanish bilingual proficiency, Team & people-oriented, Self-driven, Diligent, Perseverant.

## Education

### University of Toronto

Toronto, ON, Canada  
 Cumulative GPA 3.94 on a 4.0 scale  
 M.A.Sc. Electrical and Computer Engineering April 2020

#### Relevant IC Coursework

Integrated Circuits for Wireless Communications (ECE 1390), Analog Circuit Design I (ECE 1352), VLSI Design Methodology (ECE 1388), Analog & Mixed Signal Processing Circuits (ECE 1396), High Frequency Integrated Circuits (ECE 1365 – Audit), Advanced Analog Circuits (ECE 1371 – Audit), Digital Design for Systems on Chip (ECE 1373).

### University of Washington

Seattle, WA, USA  
 Cumulative GPA 3.54 on a 4.0 scale  
 Bachelor of Science in Electrical Engineering June 2013

#### Relevant IC Coursework

Linear IC Design (EE 473), Analog Circuit Design (EE 433).

## Engineering Experience

### Synopsys

*Sr. Analog & Mixed-signal Circuit Design Engineer*

Mississauga, ON, Canada  
 August 2020 - Present

### Intelligent Sensory Microsystems Laboratory

*Research Assistant (Supervisor: Roman Genov)*

Toronto, ON, Canada  
 January 2018 - April 2020

Development of wireless biomedical integrated circuits and systems, and flexible electrode interfaces for implantable devices. Responsible for the design of ultra-low-area-and-power data transmitter RF-ICs and clock generation ICs. Participated in two group tapeouts in 65nm CMOS and contributed seven RFIC blocks to two biomedical ASICs. Also contributed with the design of the ASIC application test board and electrode and interfacing boards. *Technical information upon request.*

V-mode Edge-combining RO-based Power-scalable TX	<i>CMOS RFIC, 65nm, Cadence Virtuoso, 3 designs</i>
I-mode Stacked Edge-combining RO-based Current-reuse TX	<i>CMOS RFIC, 65nm, Cadence Virtuoso, 2 designs</i>
ULP Programmable Prescaler for High Division Ratios	<i>CMOS AMS IC, 65nm, Cadence Virtuoso, 2 designs</i>
Microsystem Application Testboard	<i>PCB Design, Altium, 4 layers, 354 components</i>
Flexible Micro-electrode Arrays for In-vivo PNS Experiments	<i>PCB Design, Altium, 17 fabricated flex designs</i>
Depth Rigid-shank Electrodes for In-vivo CNS Experiments	<i>PCB Design, Altium, 5 fabricated rigid designs</i>
Electrode Interfacing Boards Framework	<i>PCB Design, Altium, 8 fabricated rigid designs</i>

### Ashima Devices

*Hardware/Firmware Design Engineer*

Pasadena, CA, USA  
 June 2014 - May 2015

Development of the sensor, communication and flight control hardware for the Hexpuck unmanned aerial device.

Li-Ion Active Battery Balancer Hardware Design	<i>Analog PCB, Eagle CAD, 4 layers, 176 components</i>
Li-Ion Active Battery Balancer Embedded System	<i>Firmware Development, Python, ARM-M0, Linux, GCC, GDB</i>
Flight Controller Daughter Board	<i>Circuit Design, PCB Design, Eagle CAD, 48 components</i>

Motor ESC FET Driver Power Board	<i>Circuit Design, PCB Design, Eagle CAD, 43 components</i>
Battery Simulator Hardware Design	<i>Analog Circuits, PCB Design, Eagle CAD, 16 components</i>
Power Limiter Hardware Design	<i>Analog Circuits, PCB Design, QUCS, Eagle CAD, 22 components</i>
RGB Pixels Array Board	<i>Circuit Design, PCB Design, Eagle CAD, 58 components</i>
IMU Mag/Gyro Breakout Boards	<i>Circuit Design, PCB Design, Eagle CAD, 2 fabricated designs</i>
Flight Controller Interface Board	<i>Circuit Design, PCB Design, Eagle CAD, 10 components</i>

## RTneuro Inc.

Seattle, WA, USA

*Lead Design Engineer*

July 2013 - May 2014

Design of the bio-medical sensors, the wireless embedded system and the communication software for the Rainbow wearable health device.

Wearable Wireless Health Device	<i>PCB, Altium, 4 layers, 92 components, C Firmware Development</i>
Low Power Reflectance Pulse Oximeter	<i>Analog Circuits, PCB Design, Altium Designer, Multisim</i>
Bluetooth LE Router Application	<i>Software Development, Java, Android API</i>
Electromyography Sensor	<i>Analog Circuits, PCB Design, Altium Designer, Multisim</i>
Galvanic Skin Response Sensor	<i>Analog Circuits, PCB Design, Altium Designer, Multisim</i>

## Publications

M. ElAnsary, J. Xu, J. Sales, G. Dutta, L. Long, **C. Tejeiro**, A. Shoukry, C. Tang, E. Kilinc, J. Joshi, P. Sabetian, S. Unger, J. Zariffa, P. Yoo, R. Genov, "Bidirectional Peripheral Nerve Interface With 64 Second-Order Opamp-Less  $\Sigma\Delta$  ADCs and Fully Integrated Wireless Power/Data Transmission," in **IEEE Journal of Solid-State Circuits**, vol. 56, no. 11, pp. 3247-3262, Nov. 2021.

G. O'Leary, J. Xu, L. Long, J. Sales, **C. Tejeiro**, M. ElAnsary, C. Tang, H. Moradi, P. Shah, T. Valiante and R. Genov, "A Neuromorphic Multiplier-Less Bit-Serial Weight-Memory-Optimized 1024-Tree Brain-State Classifier and Neuromodulation SoC with an 8-Channel Noise-Shaping SAR ADC Array," in 2020 IEEE **ISSCC**, Feb. 2020, pp. 402-404.

**C. Tejeiro**, C. E. Stepp, M. Malhotra, E. Rombokas, and Y. Matsuoka, "Comparison of remote pressure and vibrotactile feedback for prosthetic hand control," in 2012 4th IEEE RAS EMBS **BioRob**, Jun. 2012, pp. 521-525.

## Awards and Honors

University of Washington Dean's List ( <i>GPA of 3.50 or above</i> )	2011, 2013
University of Washington Kaiser Aluminum Scholarship ( <i>Good academic record and leadership</i> )	June, 2012
North Seattle Community College Merit Scholarship ( <i>Academic excellence</i> )	June, 2010

## Volunteer Experience

IEEE ISSCC Conference Student Volunteer (2018, 2019, 2020)	Feb. 2018, Feb. 2019, Feb. 2020
STARS Middle/High-school Tutoring Program (Lake Avenue Community Foundation)	April - June 2015
IEEE IMS/RFIC Symposium Student Volunteer	June 2013

## Teaching Experience

Engineering Strategies and Practice (ESP) Tutorial TA (APS 111, 112)	Fall 2019, Winter 2020
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